



Docket No.: 247421US2



ATTORNEYS AT LAW

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/752,504
Applicants: Yoshiyuki TANAKA, et al.
Filing Date: January 8, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT
INCLUDING OPERATION TEST CIRCUIT AND
OPERATION TEST METHOD THEREOF
Group Art Unit: 2829
Examiner:

SIR:

Attached hereto for filing are the following papers:

STATUS REQUEST FOR FIRST OFFICE ACTION (in duplicate)

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


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DOCKET NO.: 247421US2/mms



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Yoshiyuki TANAKA, et al.

SERIAL NO: 10/752,504

GROUP NO: 2829

FILED: January 8, 2004

EXAMINER:

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING OPERATION
TEST CIRCUIT AND OPERATION TEST METHOD THEREOF

FIRST OFFICE ACTION STATUS REQUEST

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

The Patent Office is respectfully requested to inform the undersigned of the
approximate date a first Official Action will be issued in this Application.

A duplicate of this letter is enclosed for your convenience.

Respectfully Submitted,

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